



PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1

of 1

Complete if Known

Application Number	10/689,380
Filing Date	20 October 2003
First Named Inventor	Mark Beaumont
Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	DB001072-000

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
JL		US- 4,816,993	03/28/1989	Takahashi	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
JL		JP 60028345 A	02/13/1985	Fujitsu LTD		

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8 MARCH 2006

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DE		US- 6,430,618 B1	08-06-2002	Karger et al.	
DE		US- 6,421,772 B1	07-16-2002	Maeda et al.	
DE		US- 6,404,439 B1	06-11-2002	Coulombe et al.	
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DE		US- 5,630,129	05-13-1997	Wheat	
DE		US- 5,581,773	12-03-1996	Glover	
DE		US- 5,535,387	07-09-1996	Matsouka et al.	
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FOREIGN PATENT DOCUMENTS

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Examiner
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Daehyun Kim, Mainak Chaudhuri, and Mark Heinrich, Leveraging Cache Coherence in Active Memory Systems, Proceedings of the 16th ACM Int'l Conference on Supercomputing, pages 2-13, New York City, USA, June 2002	
		Mainak Chaudhuri, Daehyun Kim, and Mark Heinrich, Cache Coherence Protocol Design for Active Memory Systems, Proceedings of the 2002 Int'l Conference on Parallel and Distributed Processing Techniques and Applications, pages 83-89, Las Vegas, USA, June 2002.	

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